

6

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REVISIONS			
DATE	BY	APP'D	DATE

D

D

C

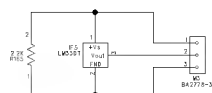
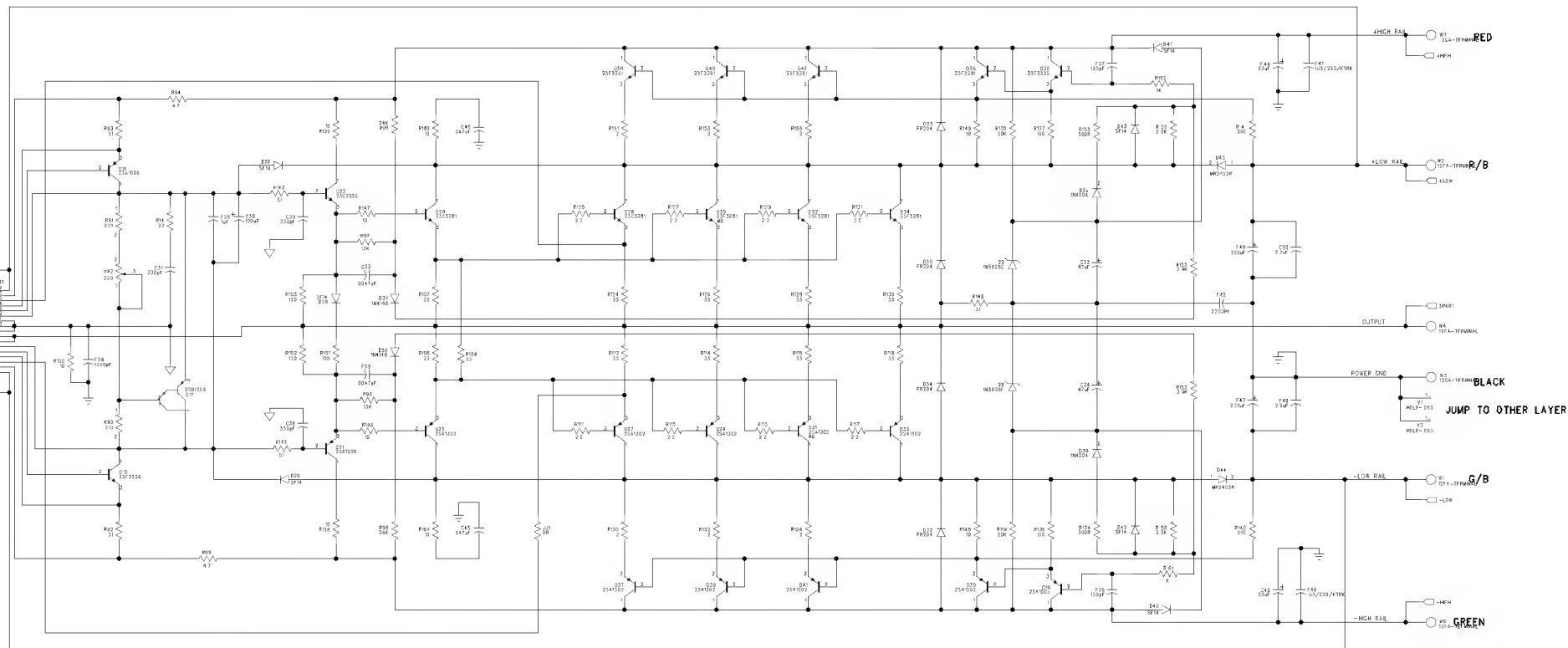
C

B

B

A

A



Q30 & Q31 NOT INSTALLED ON CA6

CRESTAUDIO

CA 6, 9 OUTPUT MODULE

DATE	BY	DATE	BY	DATE	BY

DRAWING NO. 2949-01

REV. 0

SHEET 2 OF 2

6

5

4

3

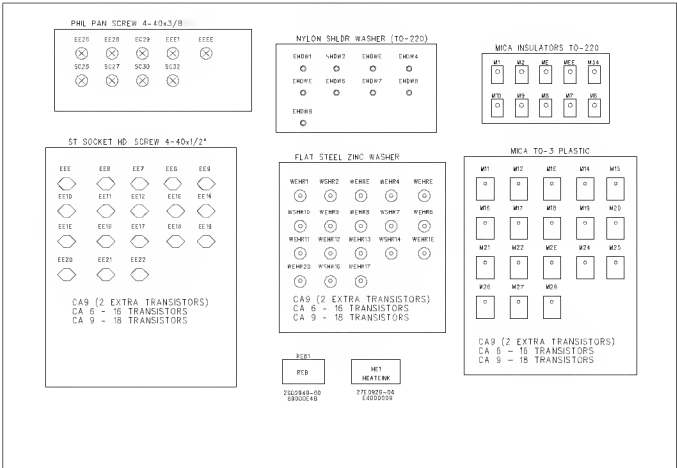
2

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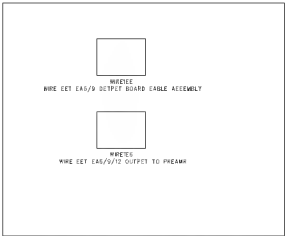
REVISION RECORD			
LTW	E (O) M3	APPROVED	DATE

REV-1 BOARDS WERE NOT SENT OUT
CA 8.9 OUTPUT WAS 7001 OUTPUT PCB
SCHEMATIC MATCHES REV-1 EXCEPT FOR VALUES
LAYOUT WAS DONE USING OLD DATABASE
SEVERAL RESISTOR PINS 1 & 2 ARE SWAPPED BETWEEN SCHEMATIC AND PCB
RESISTOR PINS FIXED ON REV-1

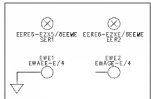
HEATSINK MODULE ASSY.



WIRE ASSEMBLY



FASTEN PREAMP TO OUTPUT PCB



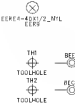
TE1
TE2

TE1
TE2

FASTEN OUTPUT PCB TO HEATSINK



LM35DP



CA 8. 9 OUTPUT MODULE

DATE	DATE	DATE	DATE	DATE

SHEET 2 OF 2